

Ultra High Precision Foil Wraparound Surface Mount Chip Resistor with Temperature Coefficient of Resistance (TCR) of ± 0.05 ppm/°C, and Load Life Stability of ± 0.005 % (50 ppm)



INTRODUCTION

Vishay VSMP2018 ultra high precision Bulk Metal® Z-Foil resistor is the result of a new concept in resistors manufacturing: A proprietary Bulk Metal Foil of known and controllable properties is applied to a special ceramic substrate. A resistive pattern is then photoetched by an ultra-fine technique developed by Vishay.

This process results in a resistor element that combines the all important characteristics of low temperature coefficient (TCR), long-term stability, ESD immunity, tight tolerance, non-inductance, low capacitance and low noise.

These characteristics are optimum, approaching in total performance the theoretical ideal, a straight wire.

The behavior of Vishay resistors under load is characterized by a very small initial resistance change which occurs within the first 500 h of the test. Only insignificant resistance changes are observed upon continuation of the test, and no signs of wear out appear even at full load over several thousands hours.

The VSMP has a full wraparound termination which ensures safe handling during the manufacturing process, as well as providing stability during multiple thermal cyclings.

Our application engineering department is available to advise and make recommendations. For non-standard technical requirements and special applications, please contact us using the e-mail address in the footer below.

TABLE 1 - TOLERANCE AND TCR VS. RESISTANCE VALUE (1)

(- 5	5 °C t	0 +	125	°C, +	25	°C	Ref.)
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RESISTANCE VALUE (Ω)	TOLERANCE (%)	TYPICAL TCR AND MAX. SPREAD (ppm/°C)			
250 to 20K	± 0.01	± 0.2 ± 1.8			
100 to < 250	± 0.02	± 0.2 ± 1.8			
50 to < 100	± 0.05	$\pm 0.2 \pm 2.8$			
25 to < 50	± 0.1	± 0.2 ± 3.8			
10 to < 25	± 0.25	± 0.2 ± 3.8			

Note

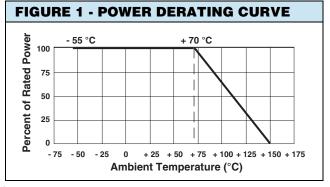
* Pb containing terminations are not RoHS compliant, exemptions may apply

FEATURES

- Temperature coefficient of resistance (TCR):
 0.05 ppm/°C typical (0 °C to + 60 °C)
 0.2 ppm/°C typical (- 55 °C to + 125 °C, + 25 °C ref.)
- Pb-free
 Available
- Resistance range: 10Ω to $20 k\Omega$ (higher values **RoHS*** from $20 k\Omega$ to $150 k\Omega$ can be supplied on request) compliant
- Vishay Foil resistors are not restricted to standard values; specific "as required" values can be supplied at no extra cost or delivery (e.g. 1K2345 vs. 1K)
- Tolerance: to ± 0.01 %
- Power coefficient "∆R due to self heating":
 5 ppm at rated power
- Power rating: to 750 mW at + 70 °C
- Load life stability: to ± 0.005 % at 70 °C, 2000 h at rated power
- Electrostatic discharge (ESD) up to 25 000 V
- Short time overload: ≤ 0.005 %
- Non-inductive, non-capacitive design
- Rise time: 1 ns effectively no ringing
- Current noise: 40 dB
- Voltage coefficient < 0.1 ppm/V
- Non inductive: < 0.08 μH
- Non hot spot design
- Terminal finish: lead (Pb)-free, tin/lead alloy
- Compliant to RoHS directive 2002/95/EC
- Matched sets are available on request
- Prototype quantities available in just 5 working days or sooner, please contact <u>foil@vishaypg.com</u>
- For better performances please contact us

APPLICATIONS

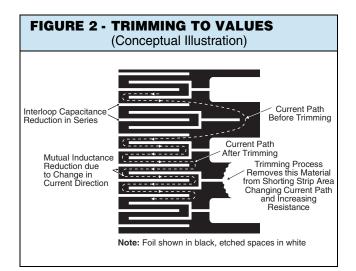
- Automatic test equipment (ATE)
- High precision instrumentation
- · Laboratory, industrial and medical
- Audic
- EB applications (electron beam scanning and recording equipment, electron microscopes)
- Down hole instrumentation
- Communication

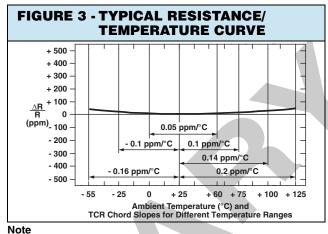


⁽¹⁾ For tighter performances and non-standard values up to 150K, please contact Vishay application engineering using the e-mail address in the footer below.

Vishay Foil Resistors







• The TCR values for < 100 Ω are influenced by the termination composition and result in deviation from this curve.

TABLE 2 - DIMENSIONS AND LAND PATTERN in inches (millimeters) **Bottom View for Mounting Recommended Land Pattern** Uncoated Ceramic **Footprint** Т THICKNESS MAXIMUM CHIP Z (1) G (1) X (1) $\pm 0.00\overline{5} (0.13)$ SIZE $\pm 0.005 (0.13)$ ± 0.005 (0.13) 2018 0.204 (5.18) 0.184 (4.67) 0.025 (0.64) 0.018 (0.46) 0.248 (6.30) 0.146 (3.71) 0.184 (4.67)

Note

(1) Land pattern dimensions are per IPC-7351A.

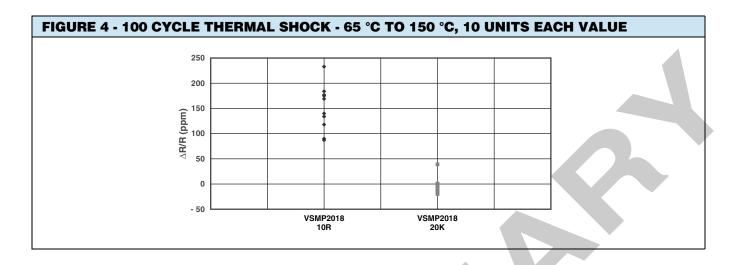
TABLE 3 - SPECIFICATIONS						
CHIP SIZE	RATED POWER (mW) at + 70 °C	MAX. WORKING VOLTAGE	RESISTANCE RANGE (Ω)	MAXIMUM WEIGHT (mg)		
2018	750	$\leq \sqrt{P \times R}$	10 to 20K	40		

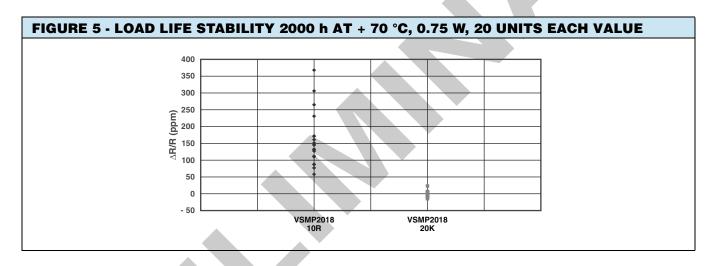
TABLE 4 - PERFORMANCES					
TEST (CONDITIONS PER MIL-PRF-55342)	MIL-PRF-55342 CHARACTERISTIC E ∆R LIMITS	TYPICAL ∆R LIMITS	MAXIMUM AR LIMITS (1)		
Thermal Shock, 100 x (- 65 °C to + 150 °C)	± 0.1 %	± 0.005 % (50 ppm)	± 0.01 % (100 ppm)		
Low Temperature Operation, - 65 °C, 45 min at 0.75 W	± 0.1 %	± 0.005 % (50 ppm)	± 0.01 % (100 ppm)		
Short Time Overload, 6.25 x 0.75 W, 5 s	± 0.1 %	± 0.005 % (50 ppm)	± 0.01 % (100 ppm)		
High Temperature Exposure, 150 °C, 100 h	± 0.1 %	± 0.01 % (100 ppm)	± 0.02 % (200 ppm)		
Resistance to Soldering Heat	± 0.2 %	± 0.005 % (50 ppm)	± 0.01 % (100 ppm)		
Moisture Resistance	± 0.2 %	± 0.005 % (50 ppm)	± 0.02 % (200 ppm)		
Load Life Stability, + 70 °C for 2000 h at Rated Power	± 0.5 %	± 0.005 % (50 ppm)	± 0.01 % (100 ppm)		

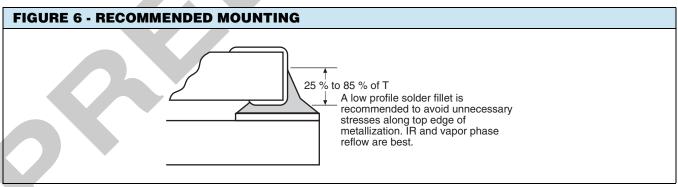
Note

 $^{(1)}\,$ As shown + 0.01 Ω to allow for measurement errors at low values.









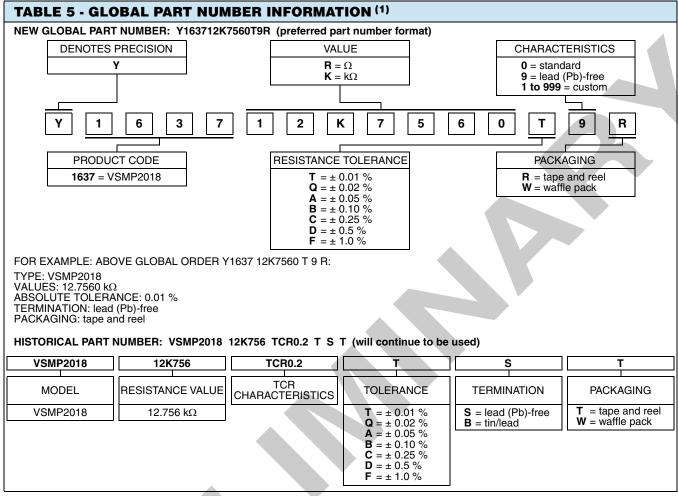
Notes

- · Avoid the use of cleaning agents which could attack epoxy resins, which form part of the resistor construction
- · Vacuum pick up is recommended for handling
- Soldering iron may damage the resistor

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Vishay Foil Resistors





Note

(1) For non-standard requests, please contact application engineering.

Legal Disclaimer Notice



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